

90

94

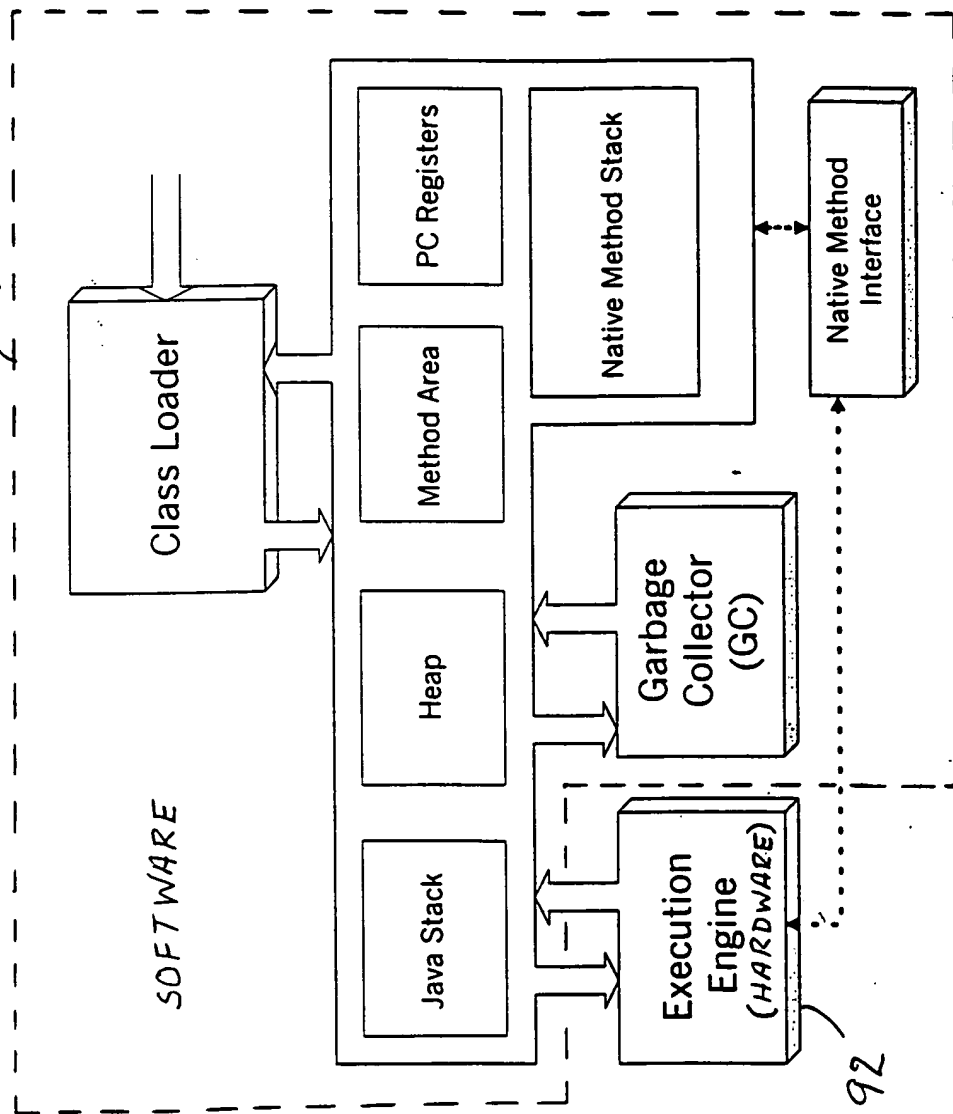


FIG. 1

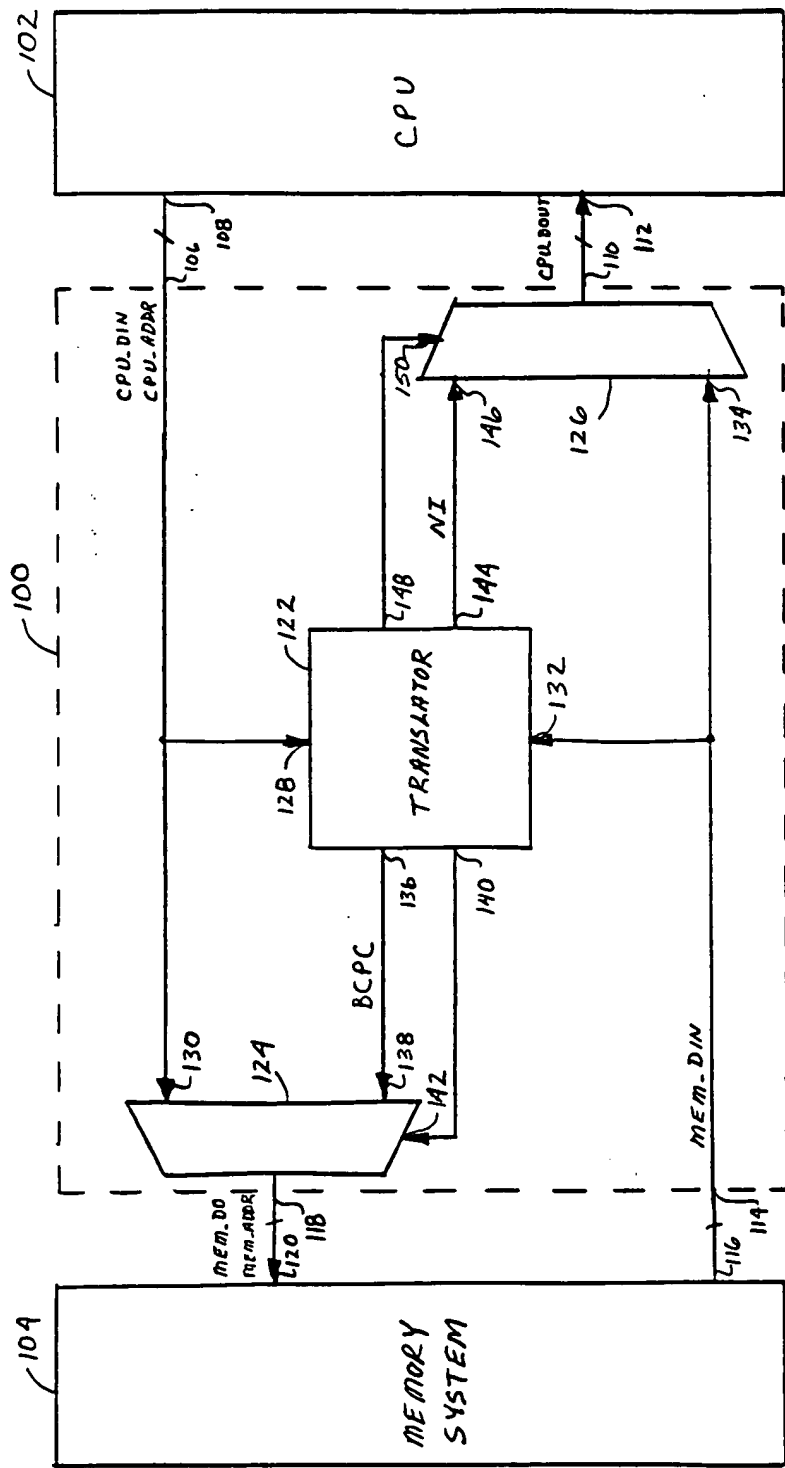


FIG. 2

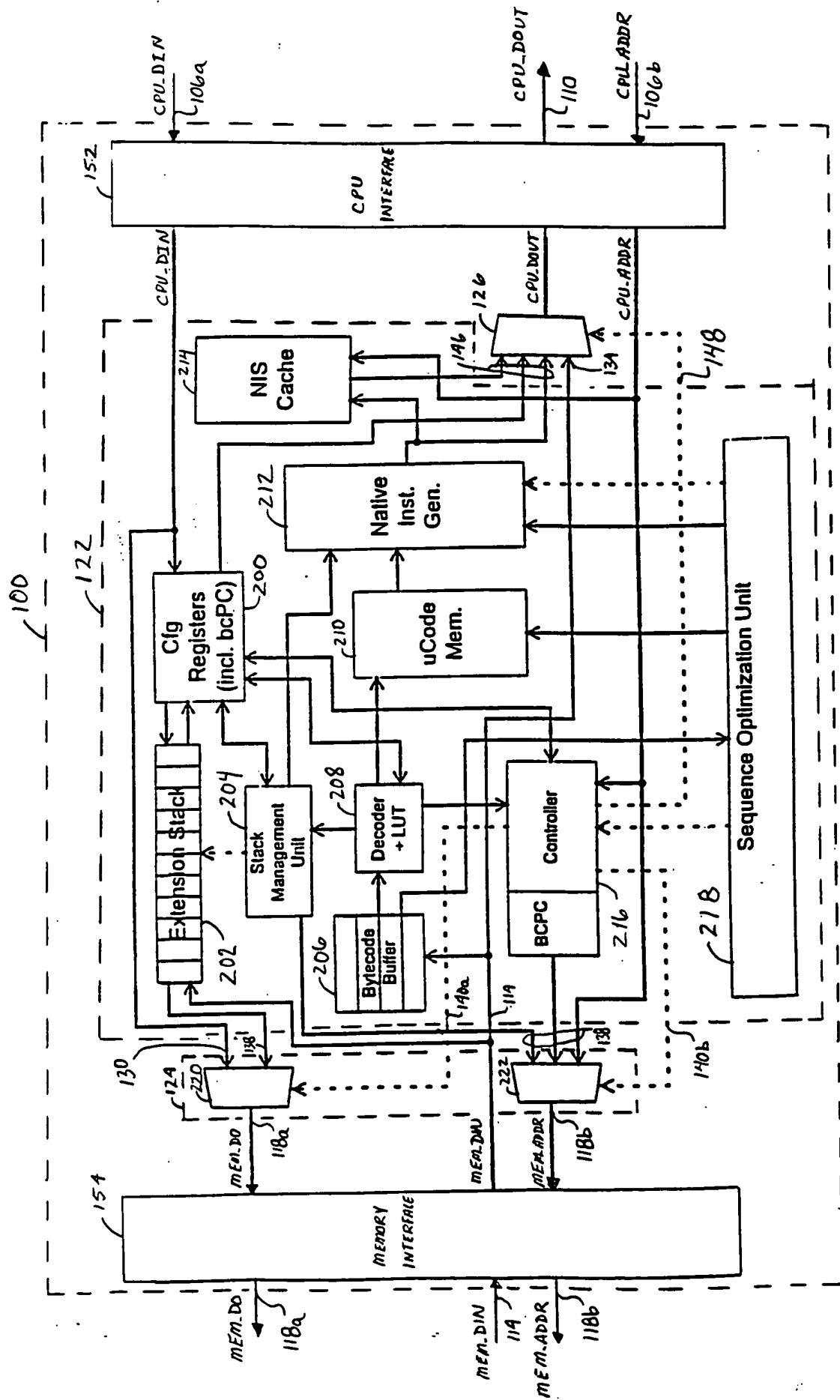


FIG. 3

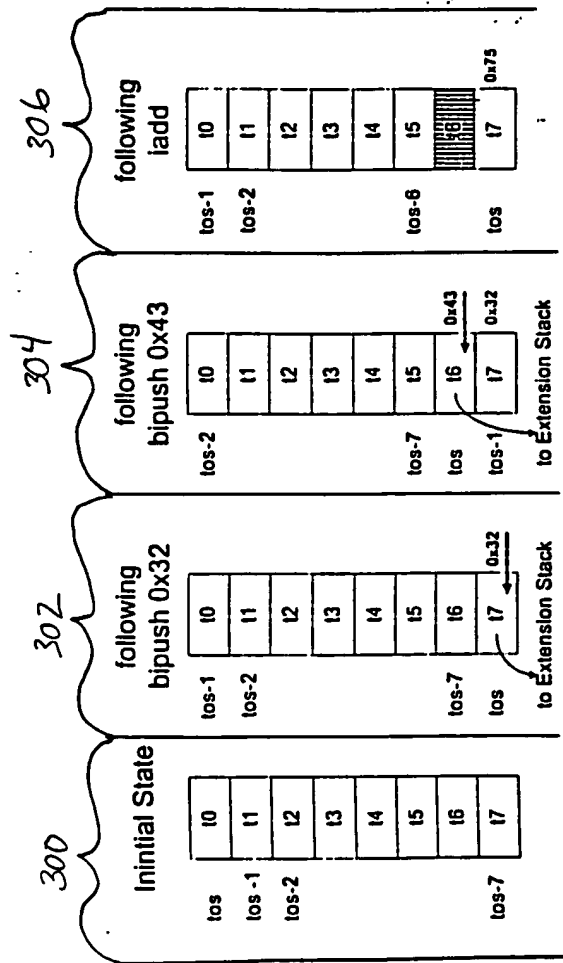


Figure 3 Stack Behavior

FIG. 4

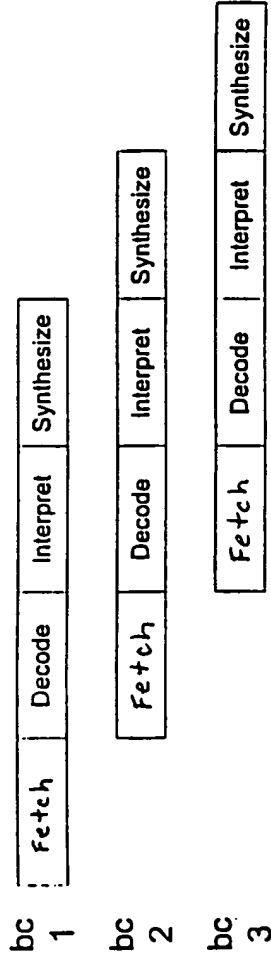


FIG. 5

	Cycle #1	Cycle #2	Cycle #3	Cycle #4	Cycle #5	Cycle #6	Cycle #7	Cycle #8	Cycle #9	Cycle #10
bcPC	00	04	08	36						
350 { Fetch	00-03	04-07	08-11							
Decode		00	01-02	03-05	06-08					
Interpret			00	01-02	03-05	06-08				
Synth.				00	01-02	03-05	06-08			
352 { Fetch				36-3A						
Decode					37					
354 { PC					3B	3C	3D	3E	3F	40
IR					3B	3C	3D	3E	3F	40

FIG. 6

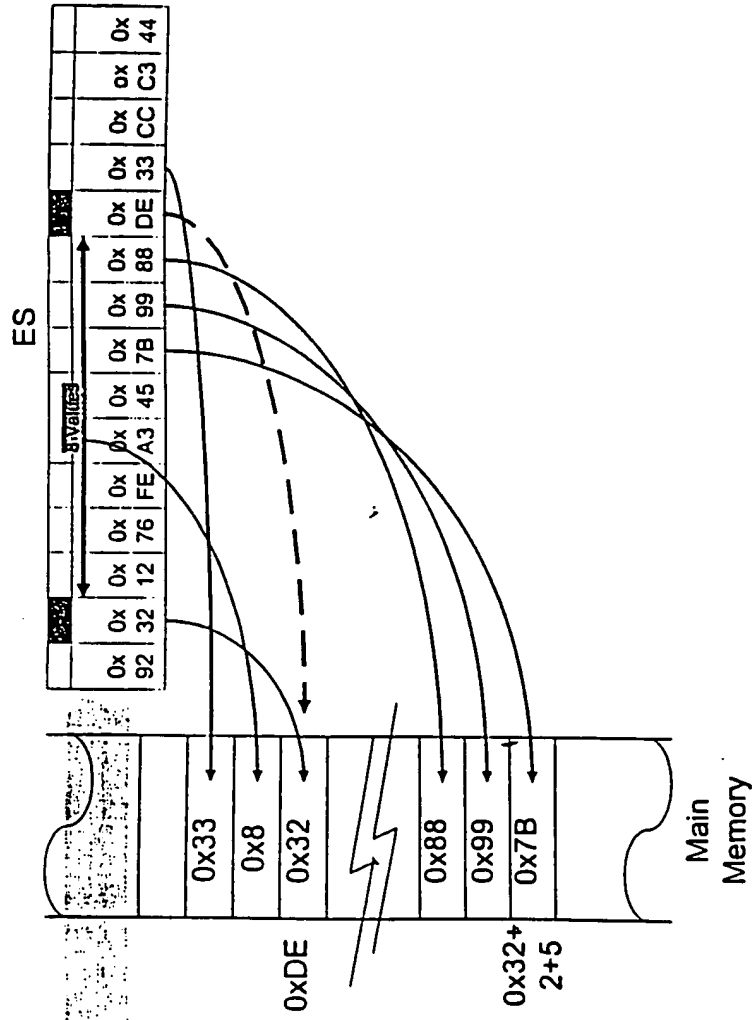


FIG. 7

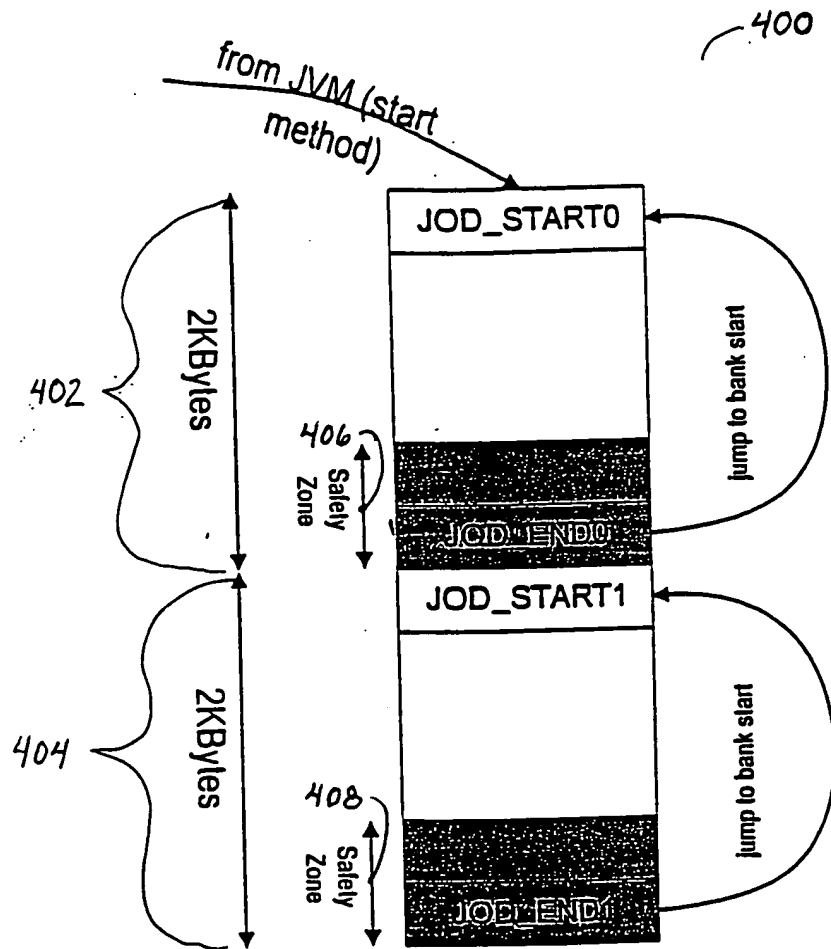


FIG. 8

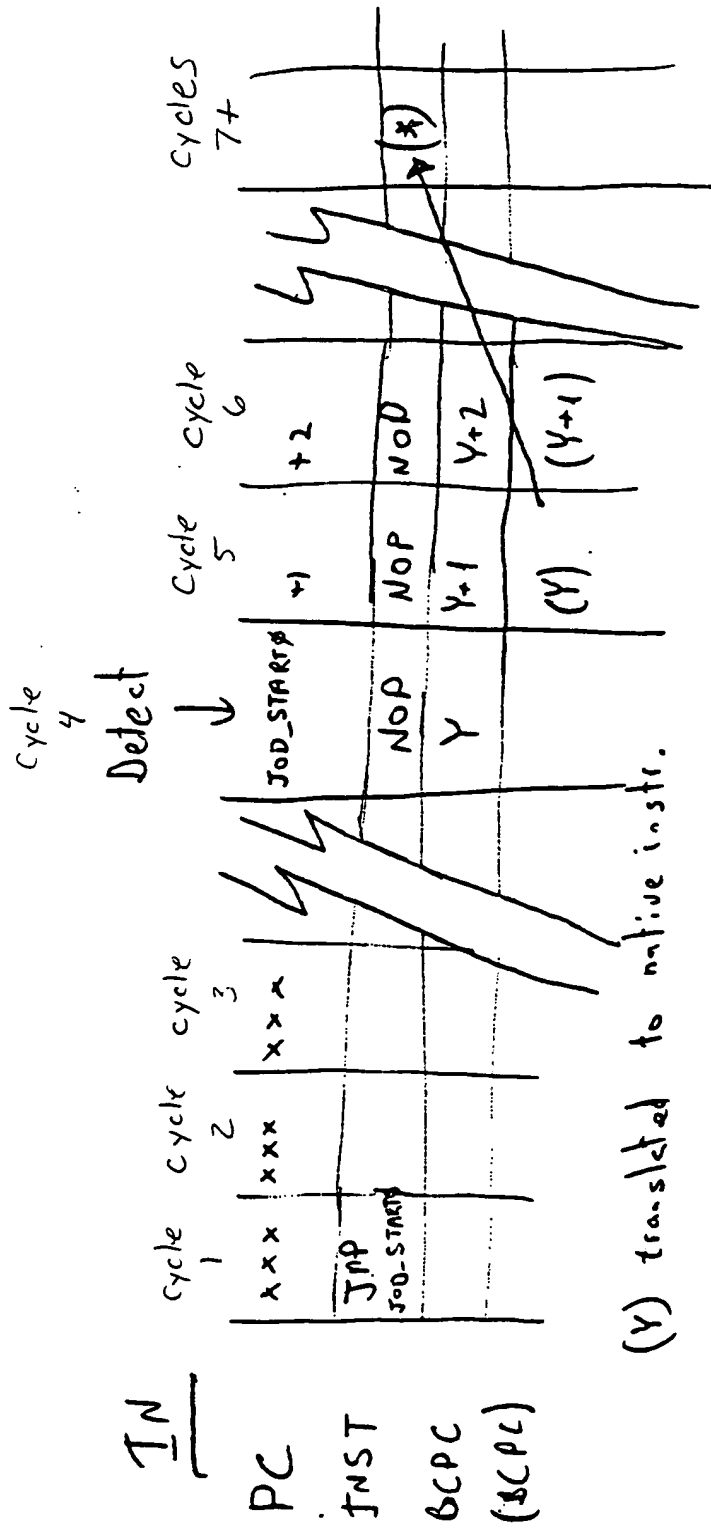


FIG. 9a

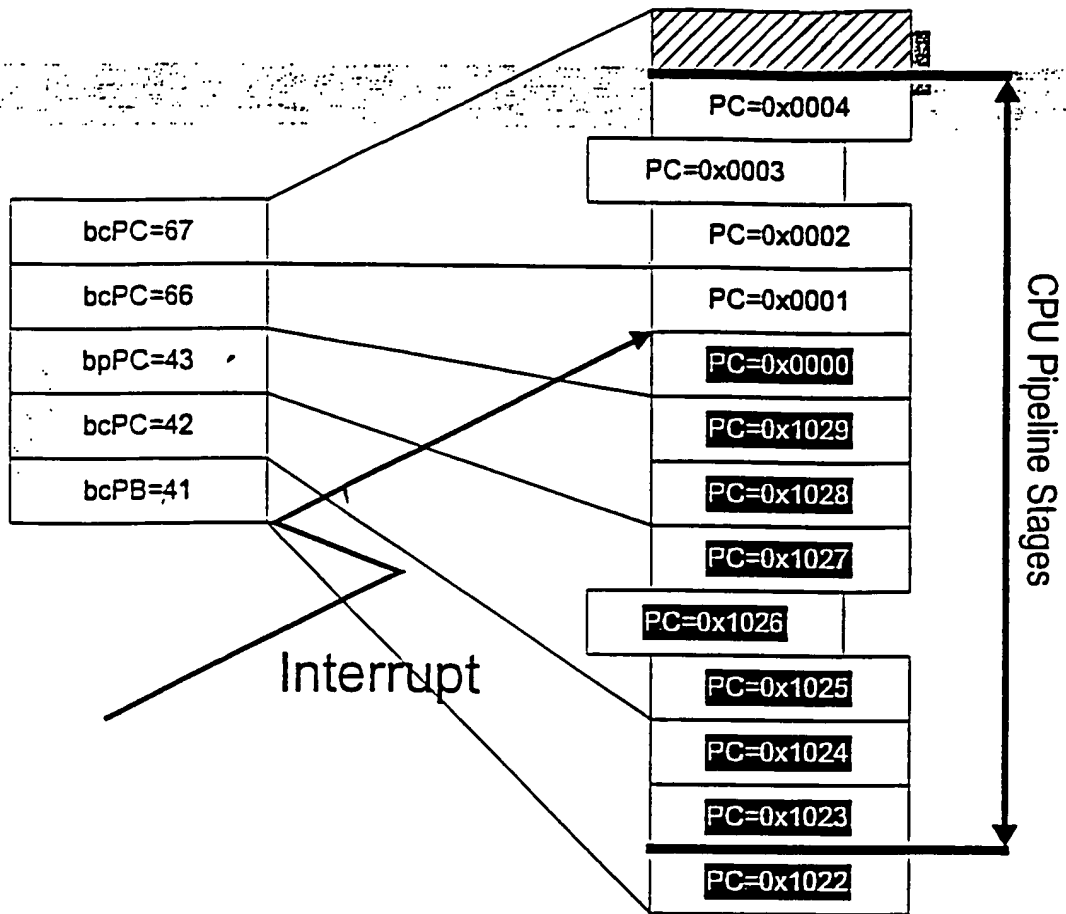


FIG. 11